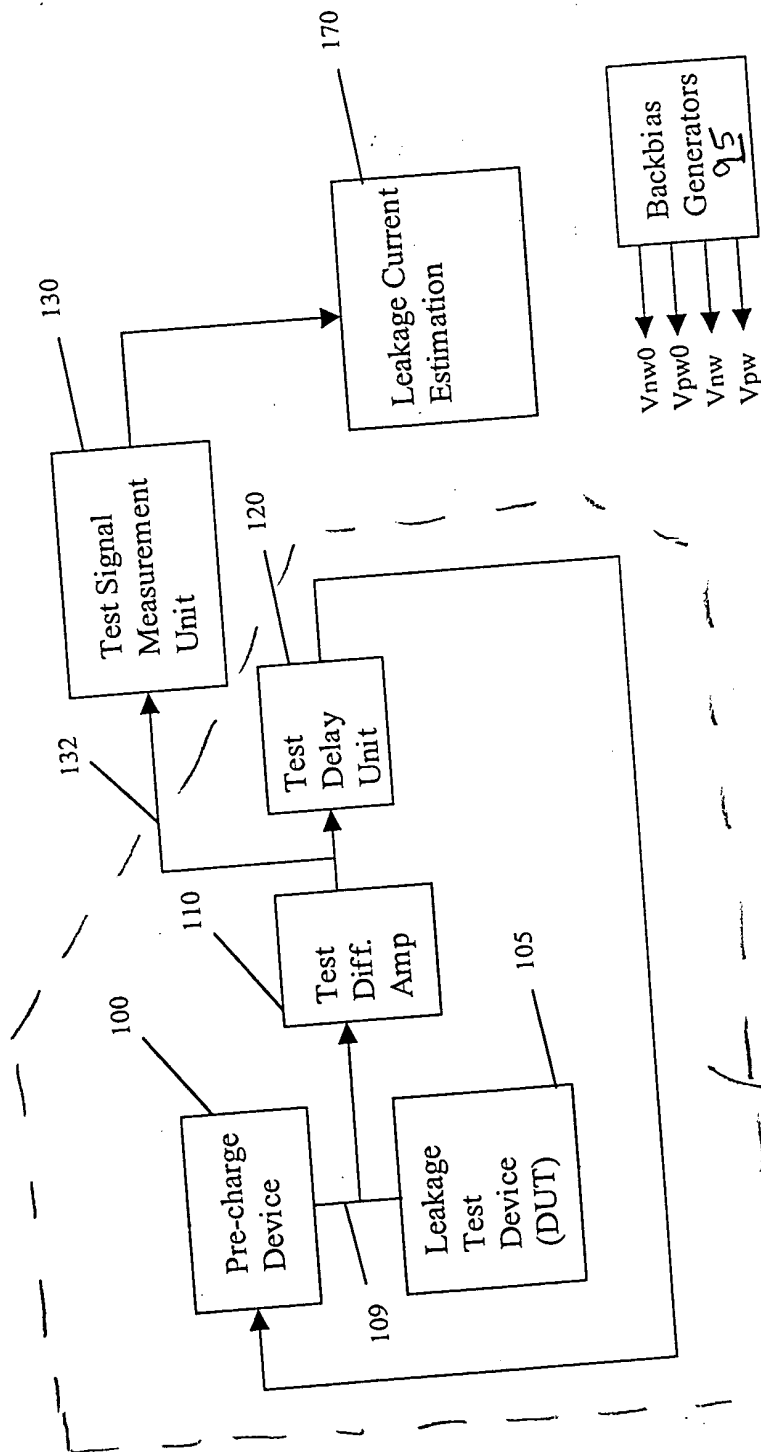


Figure 1

50



Ring Oscillator 90

Figure 2

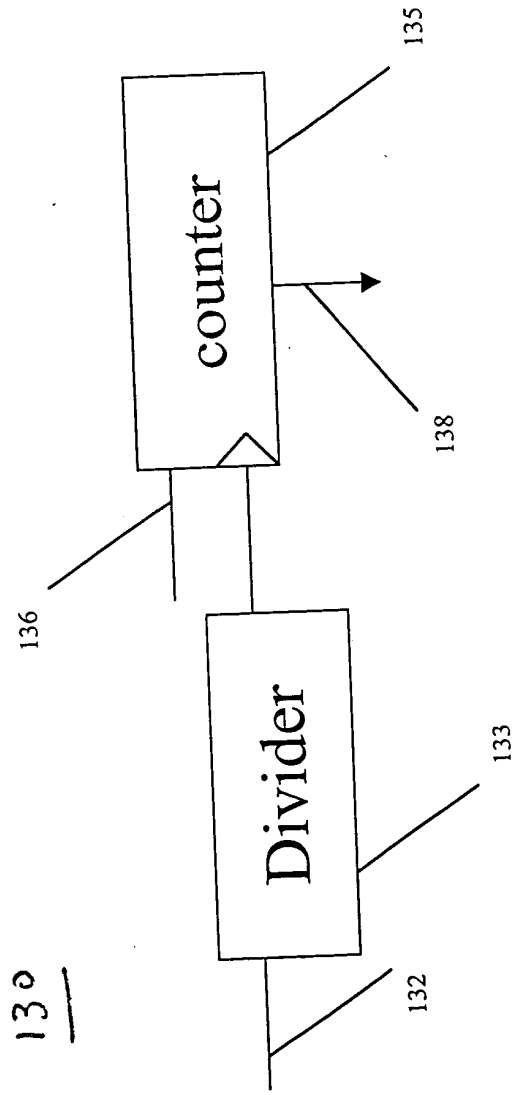




Figure 4

50

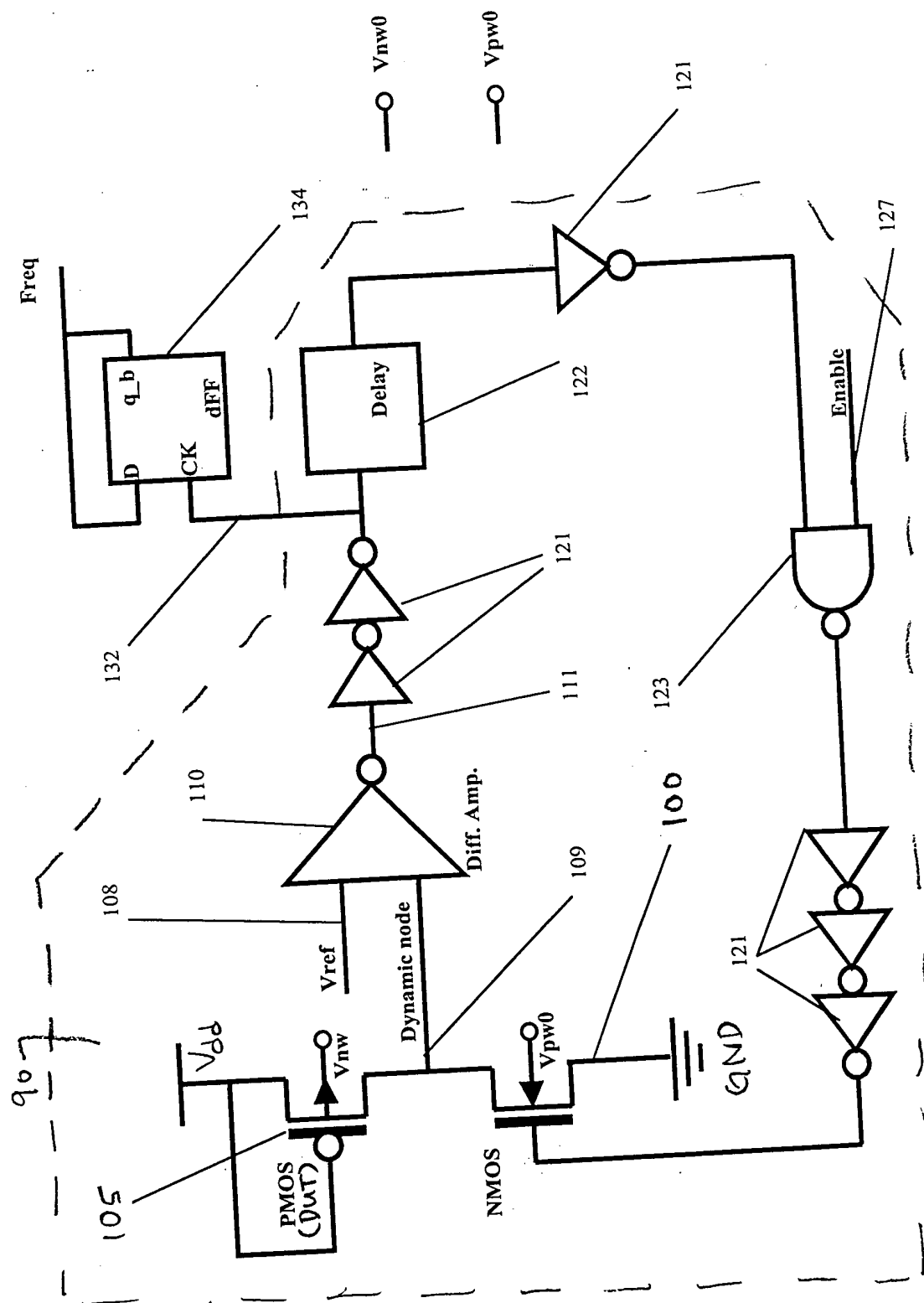


Figure 5

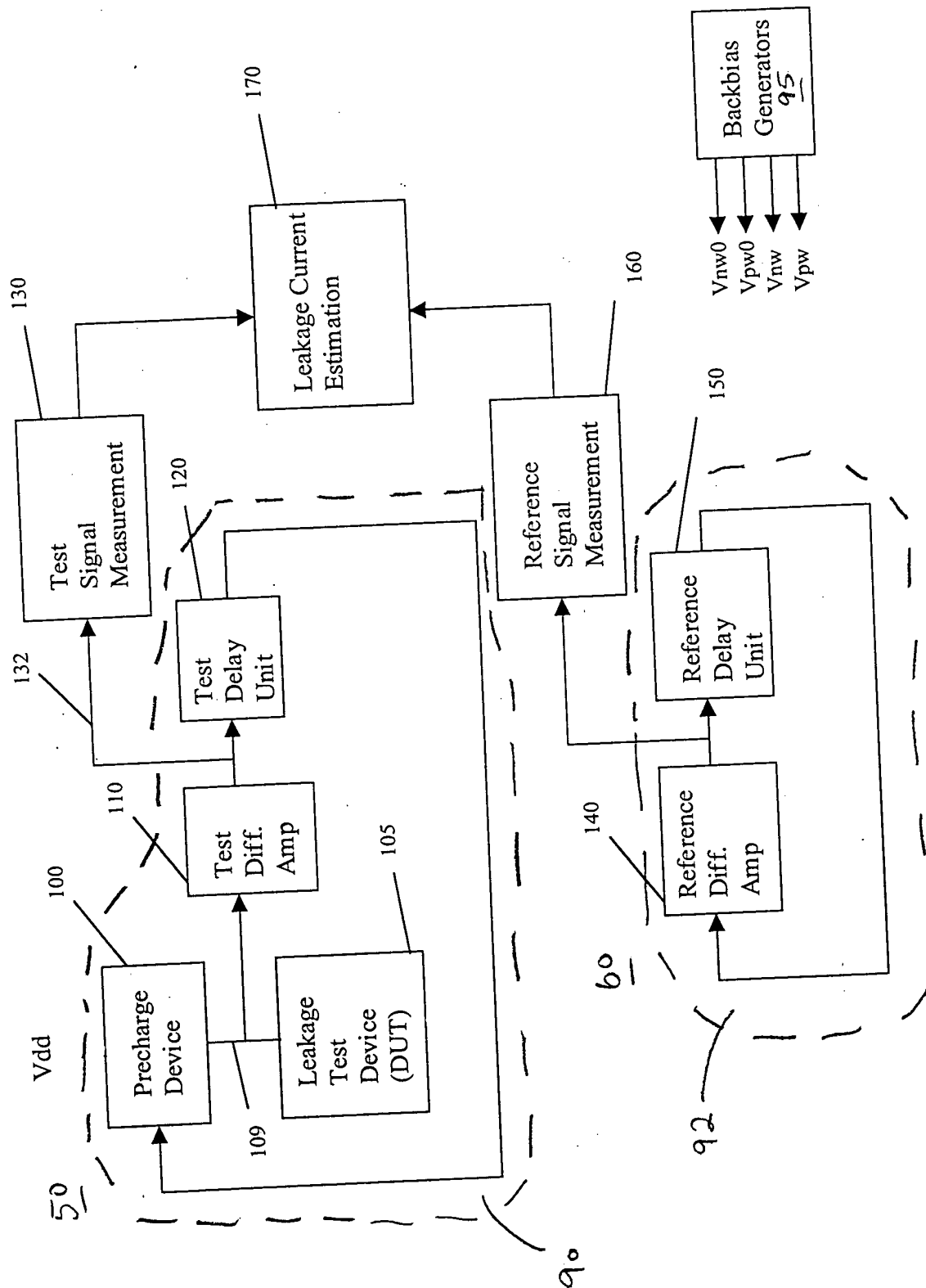


Figure 6

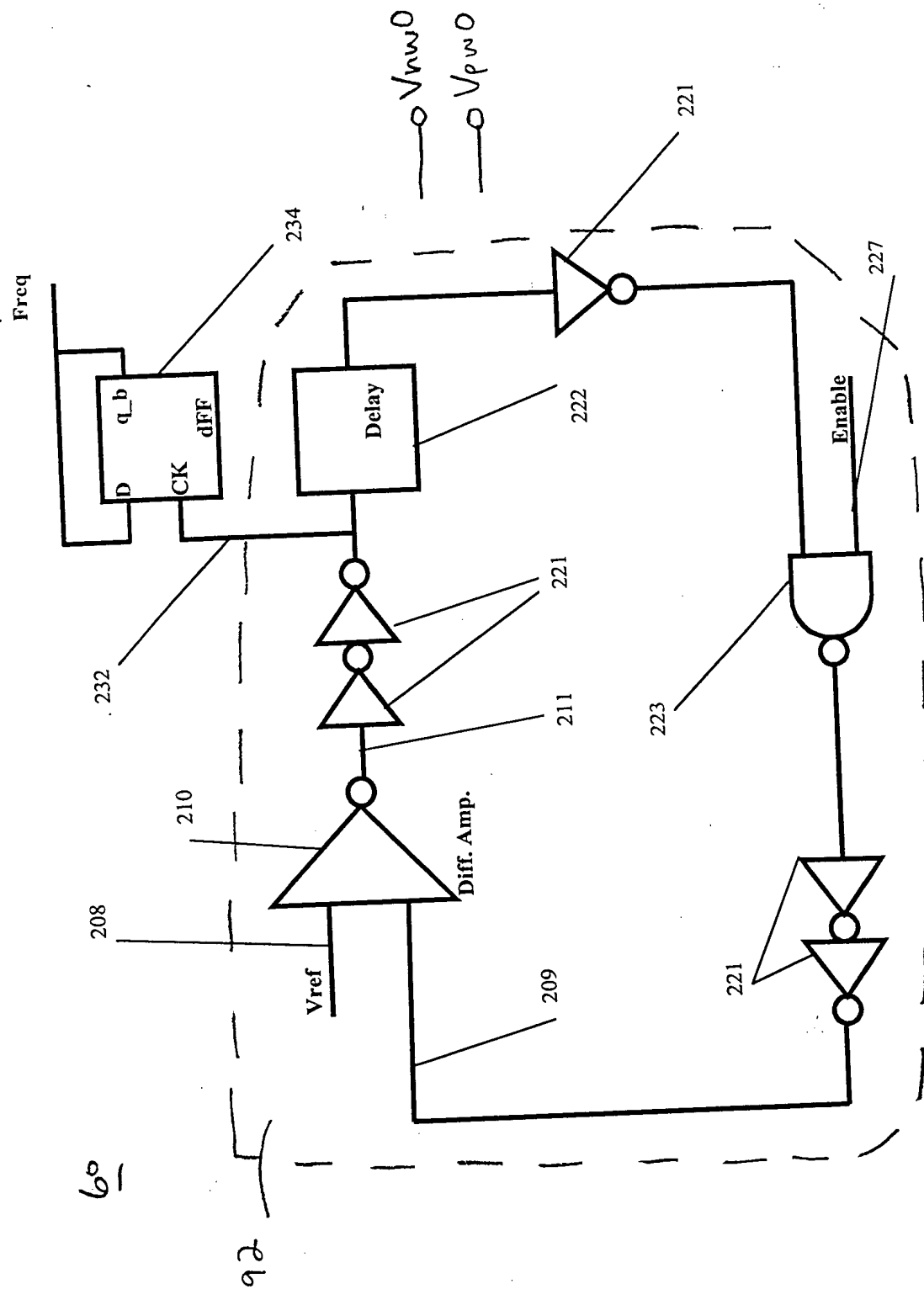
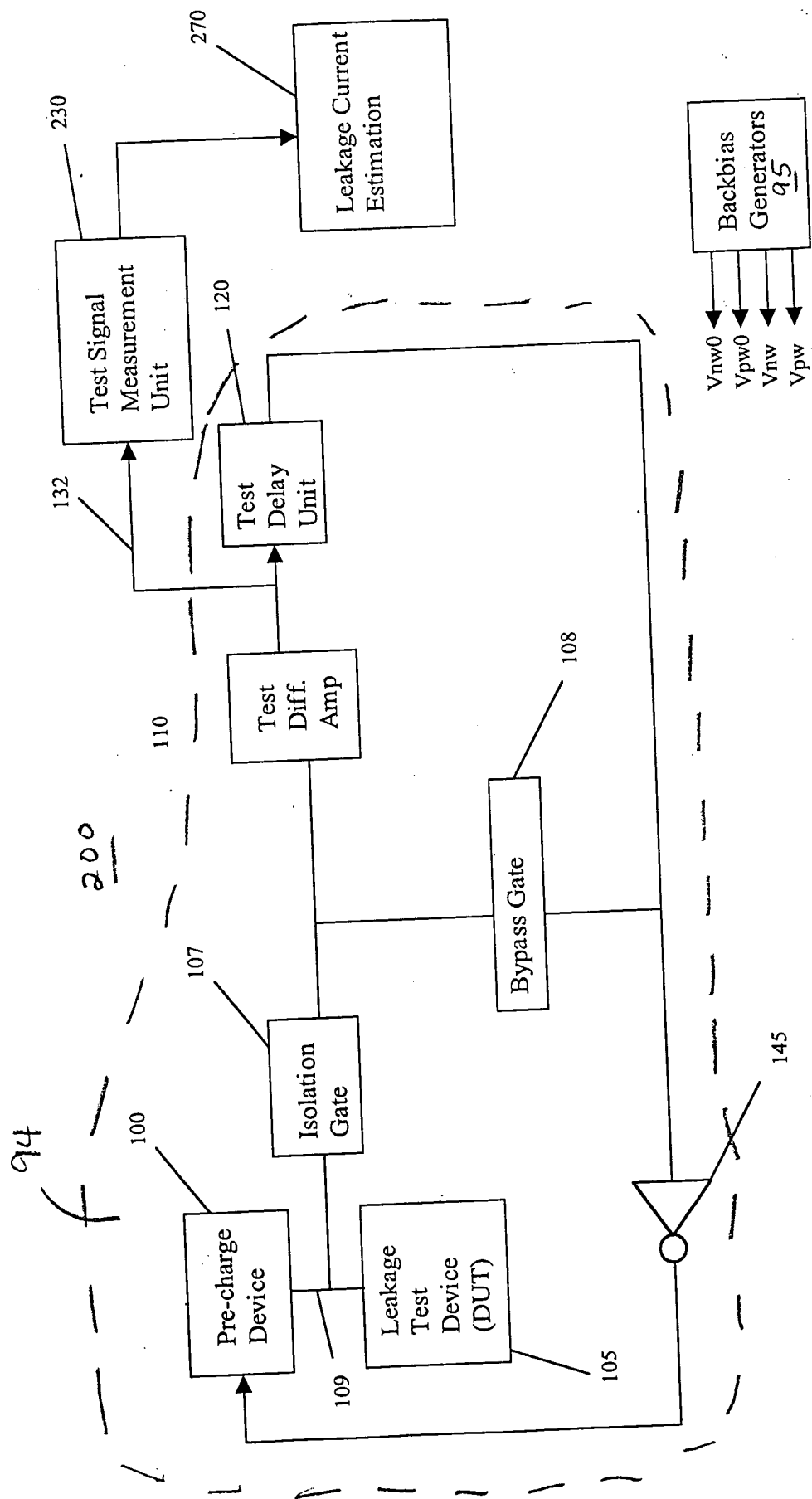


Figure 7



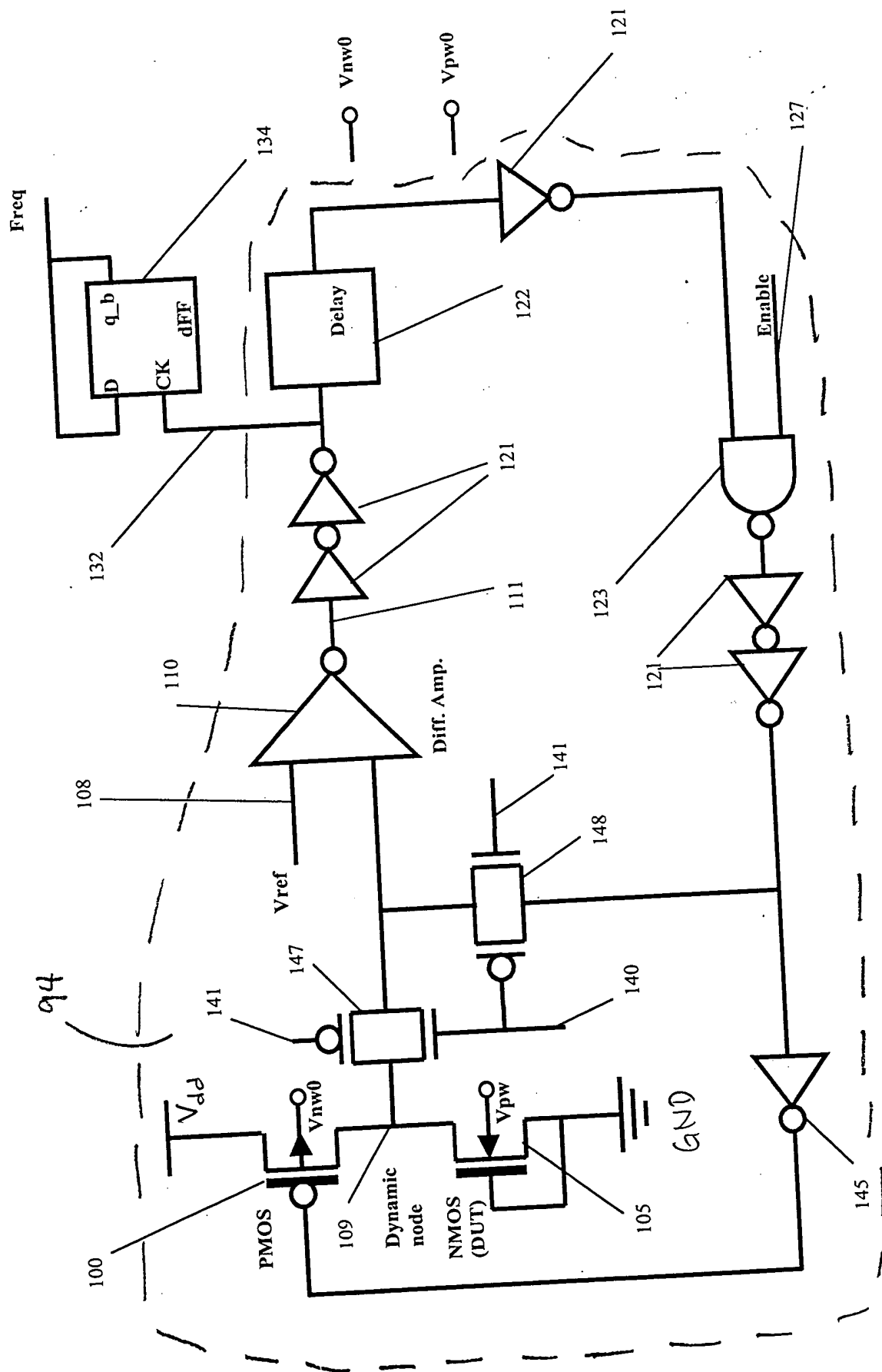
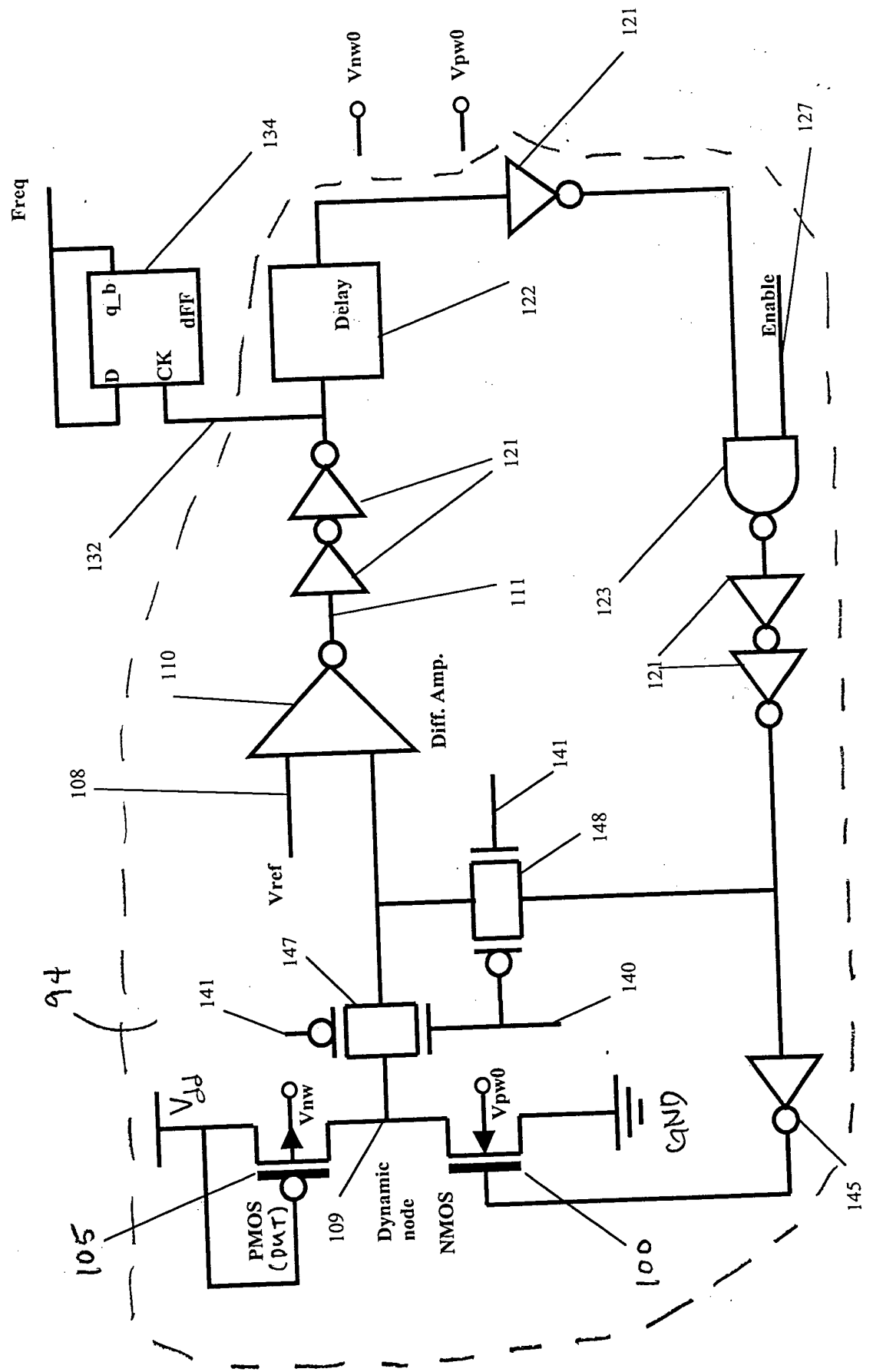




Figure 9



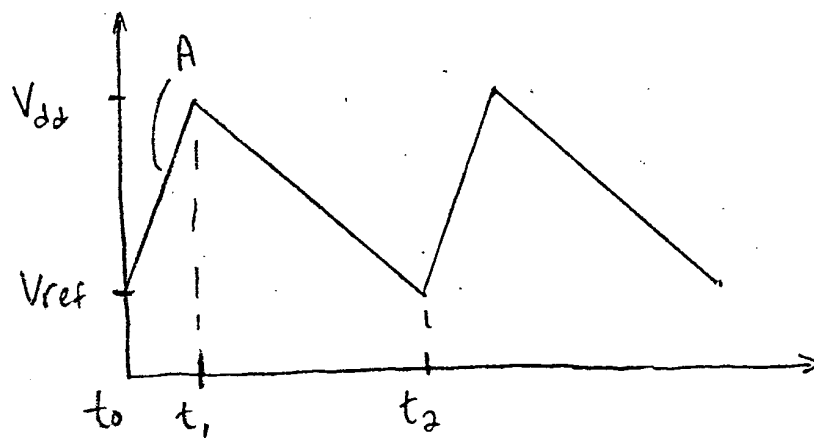


Figure 10A

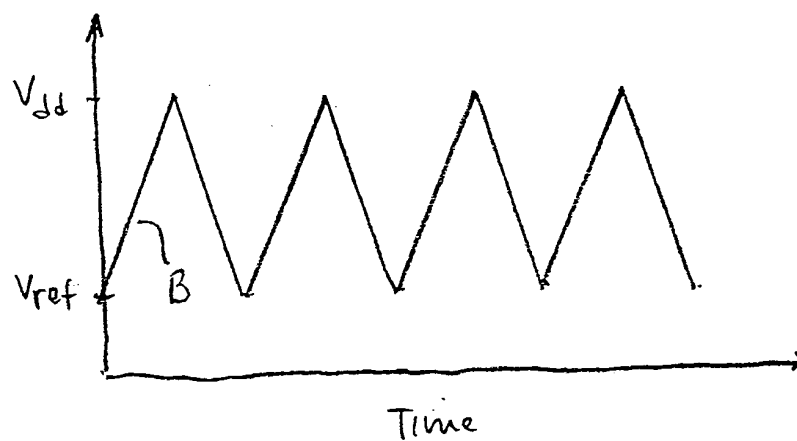
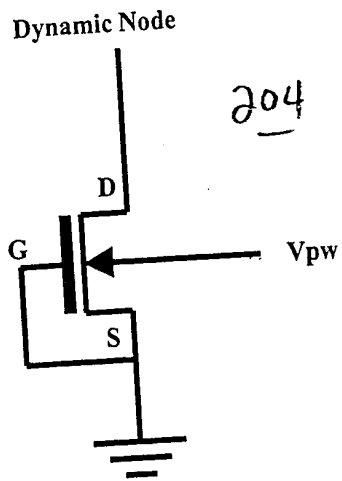
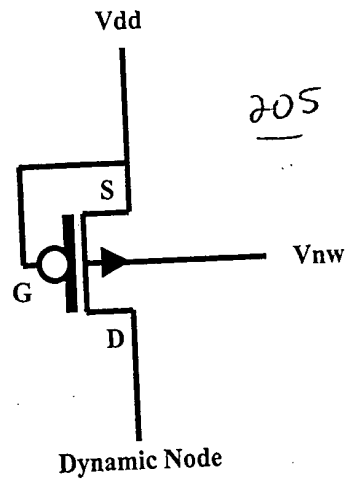


Figure 10B



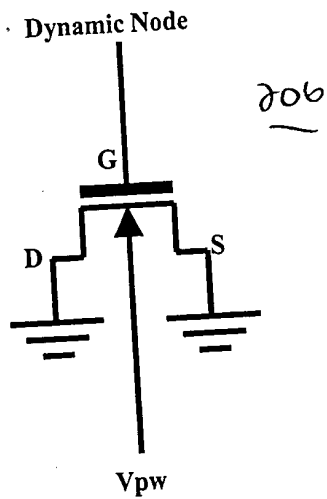
NMOS  
Ioff Current

Figure 11



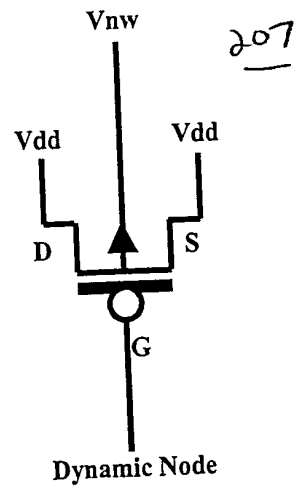
PMOS  
Ioff Current

Figure 12



NMOS Gate  
Leakge Current

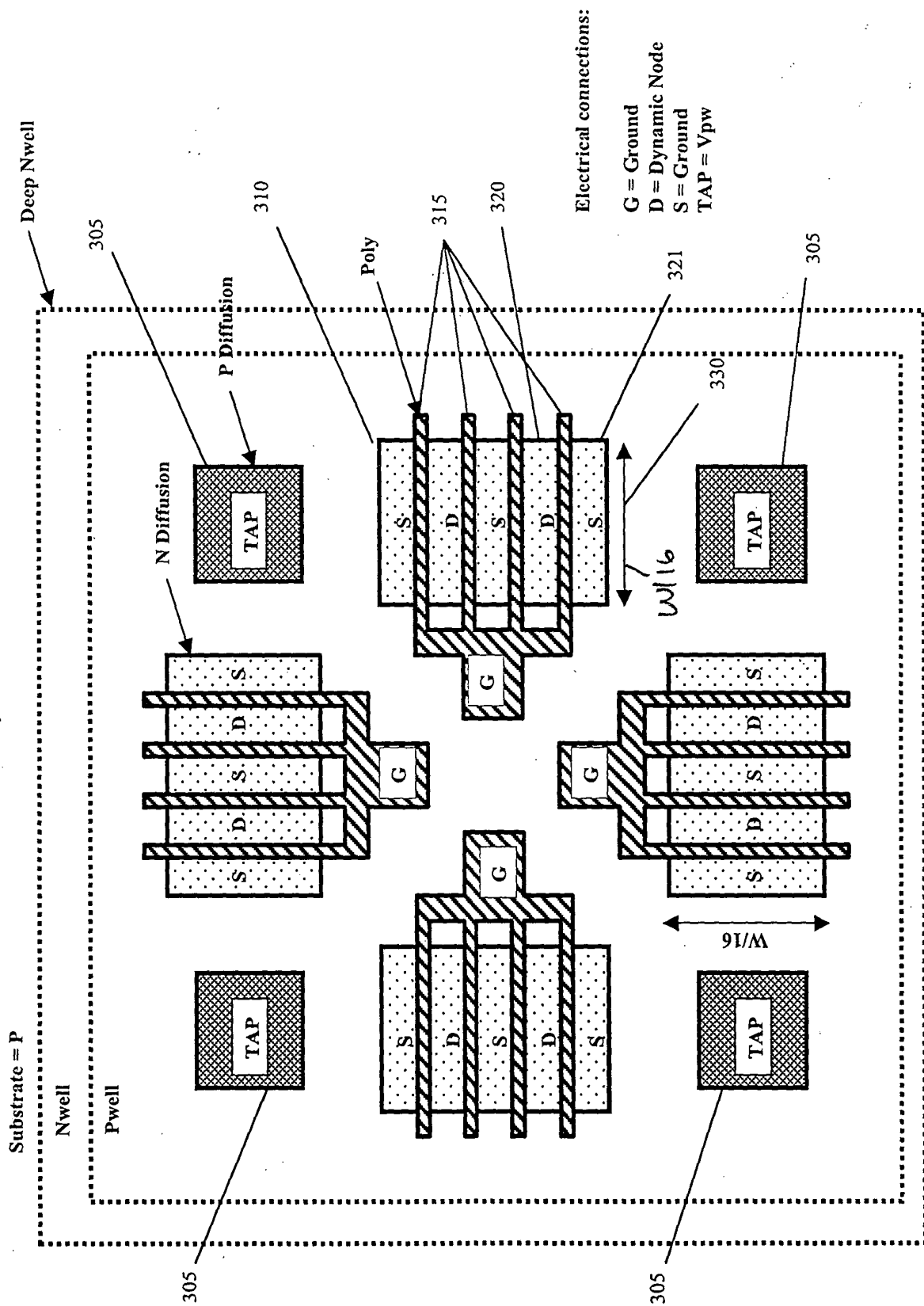
Figure 13



PMOS Gate  
Leakge Current

Figure 14

Figure 15



Substrate = P

350

355

N well

N Diffusion

360

P Diffusion

TAP

Poly

365

370

371

380

355

W/6

9W/16

Electrical

G = Vdd

S = Vdd

D = Dynan

TAP = Vn

**C = Vdd**  
**S = Vdd**  
**D = Dynamic Node**  
**TAP = Vnw**

Figure 17

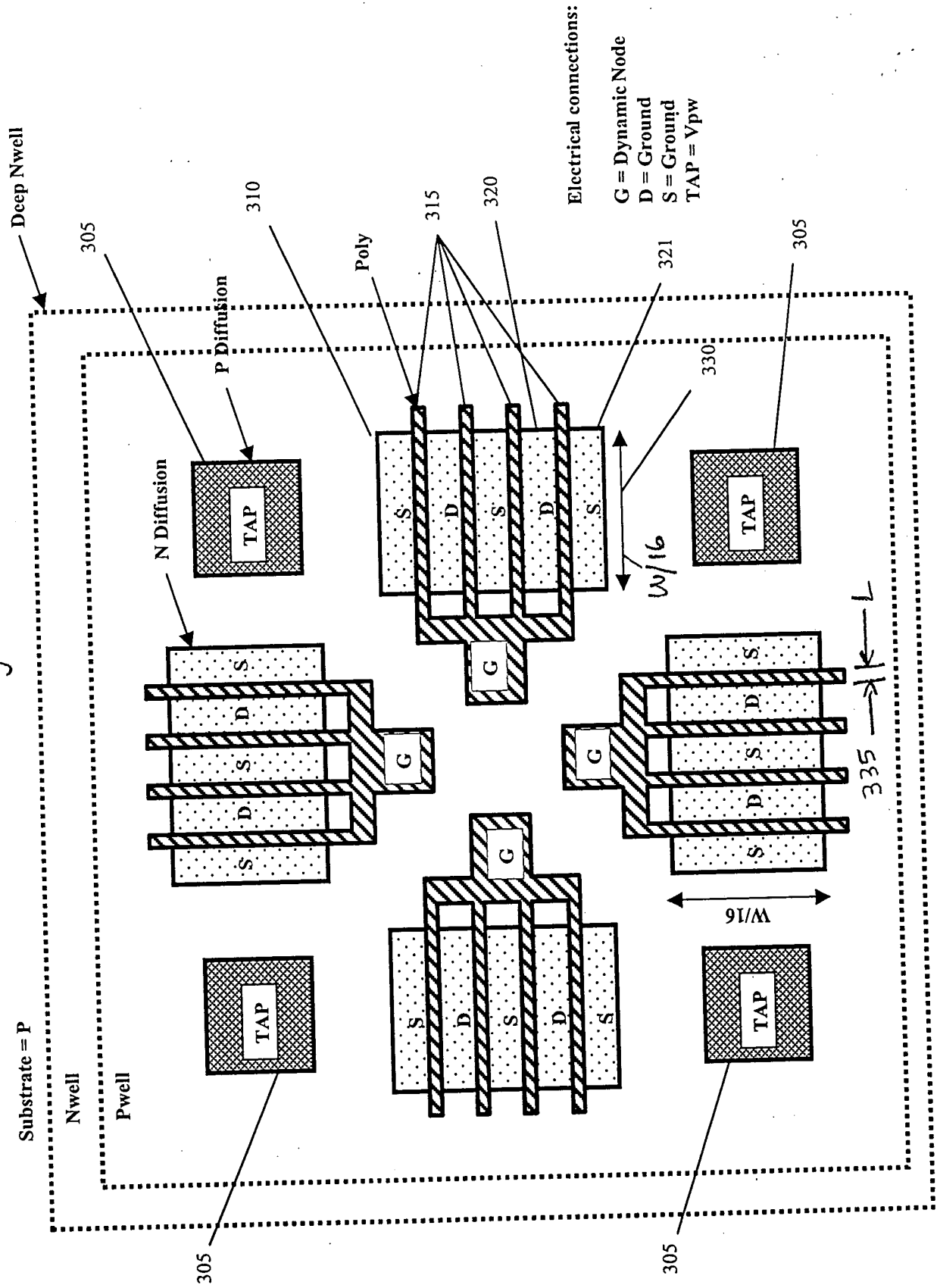
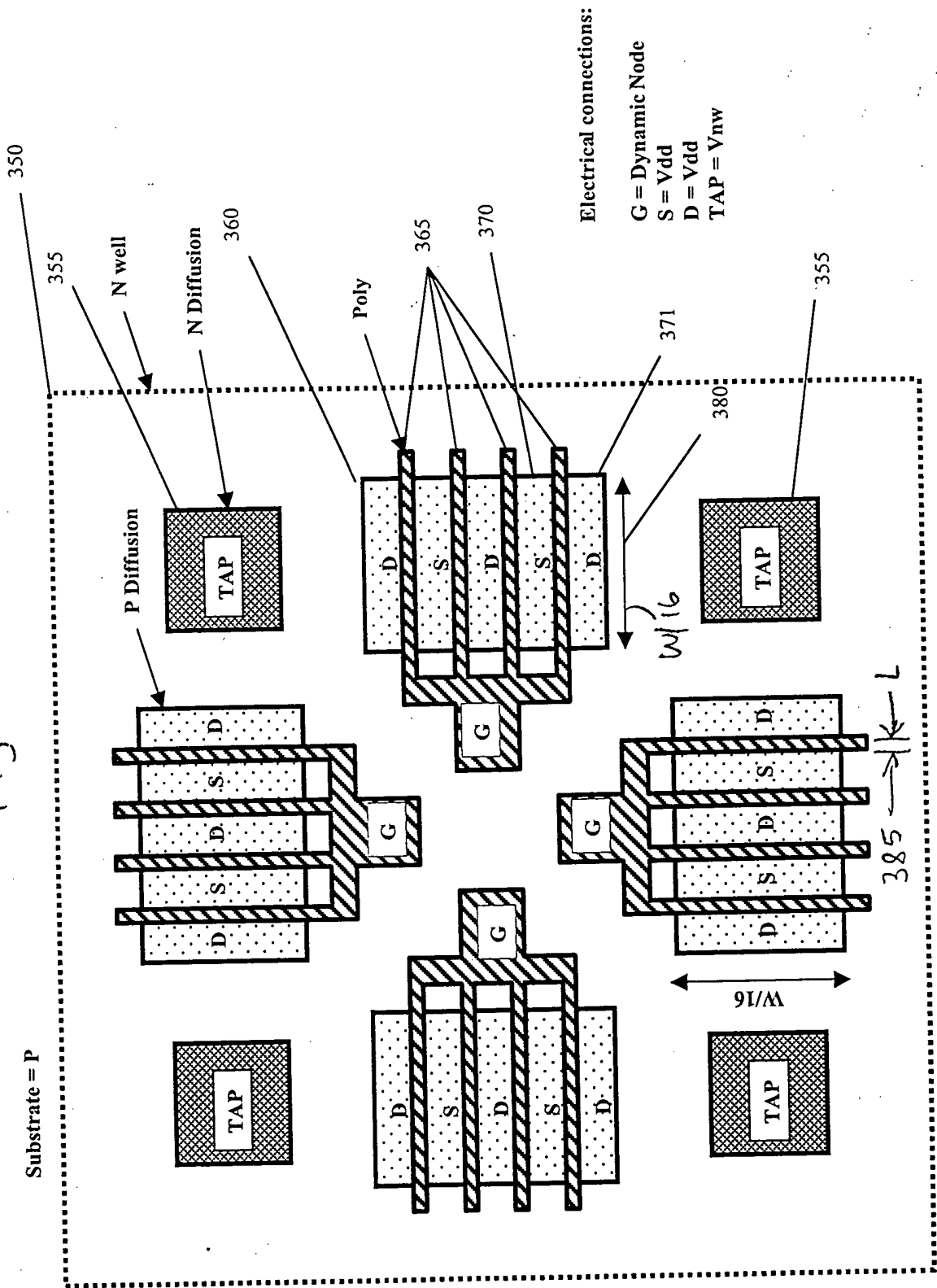


Figure 18



400

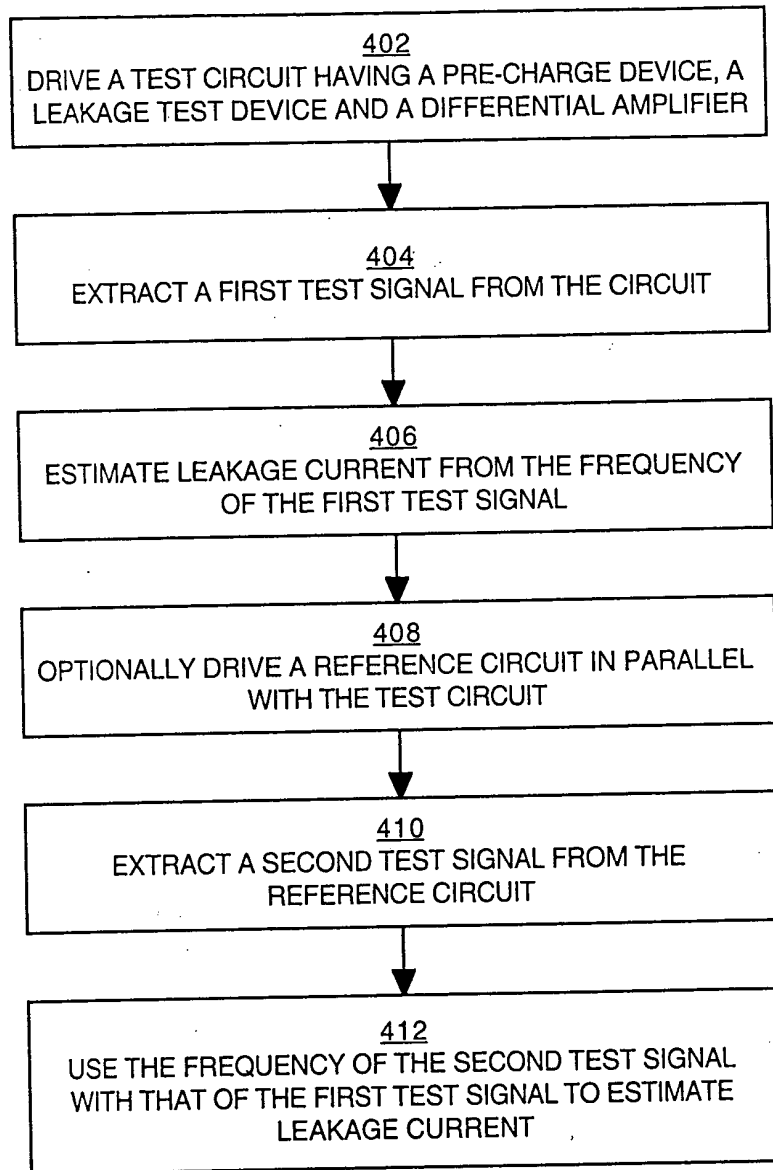


Figure 19